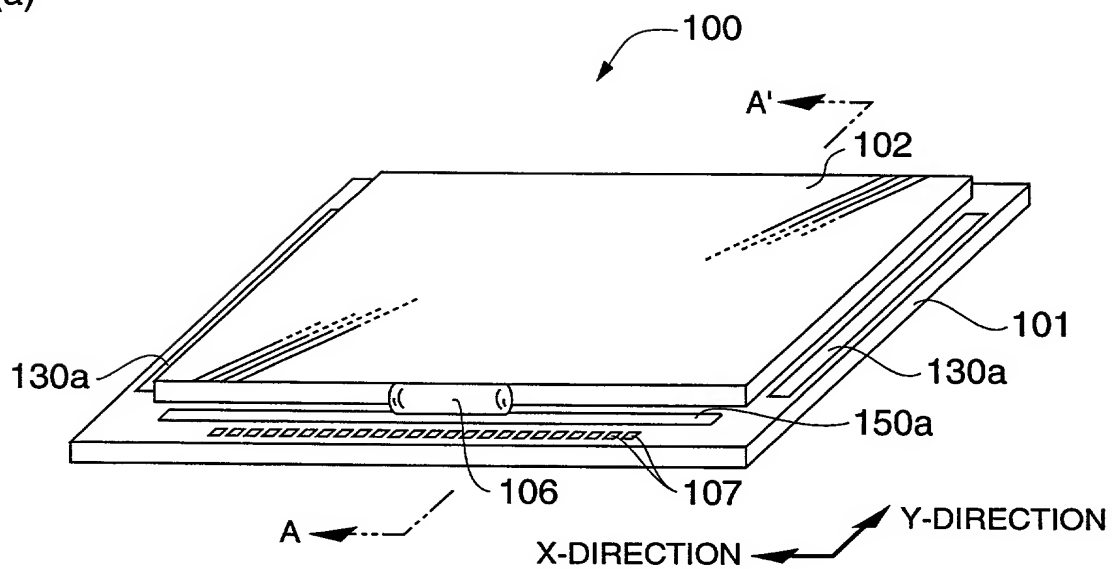


(a)



(b)

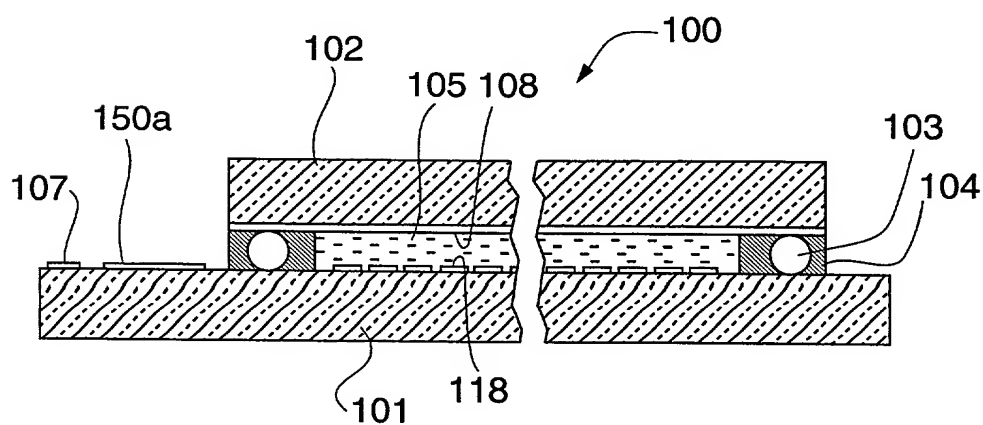


FIG. 1

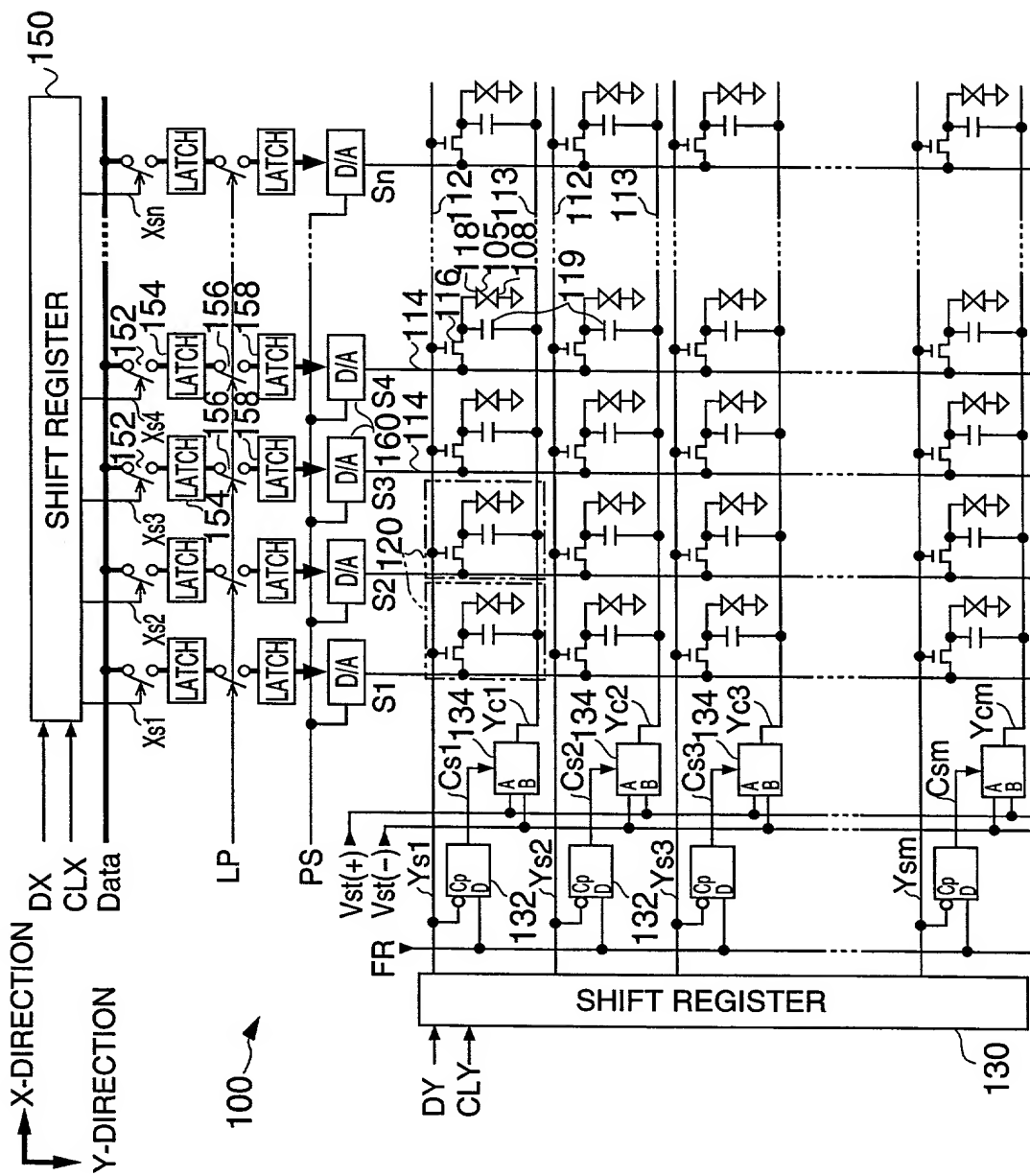


FIG. 2

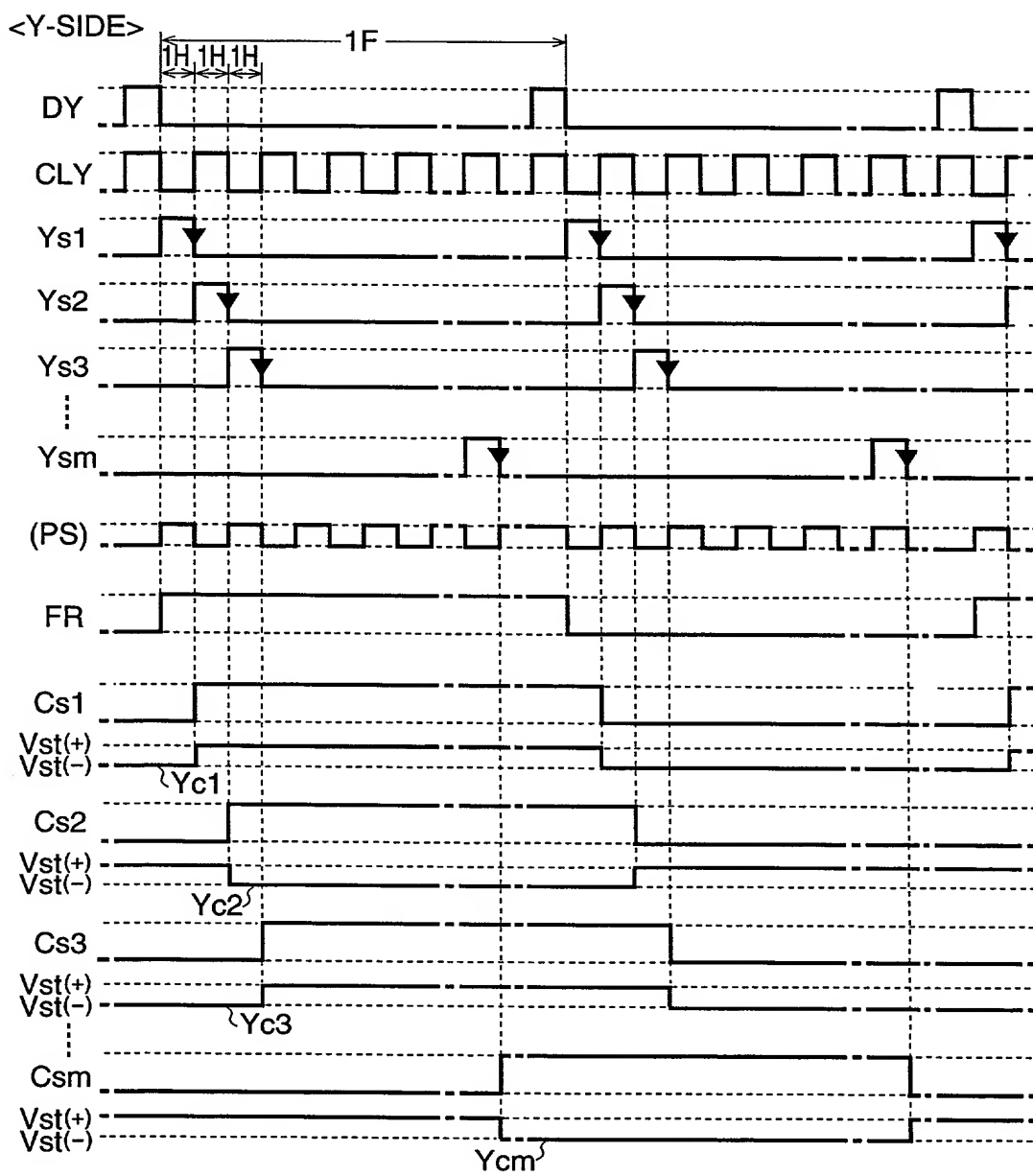


FIG. 3

<X-SIDE>

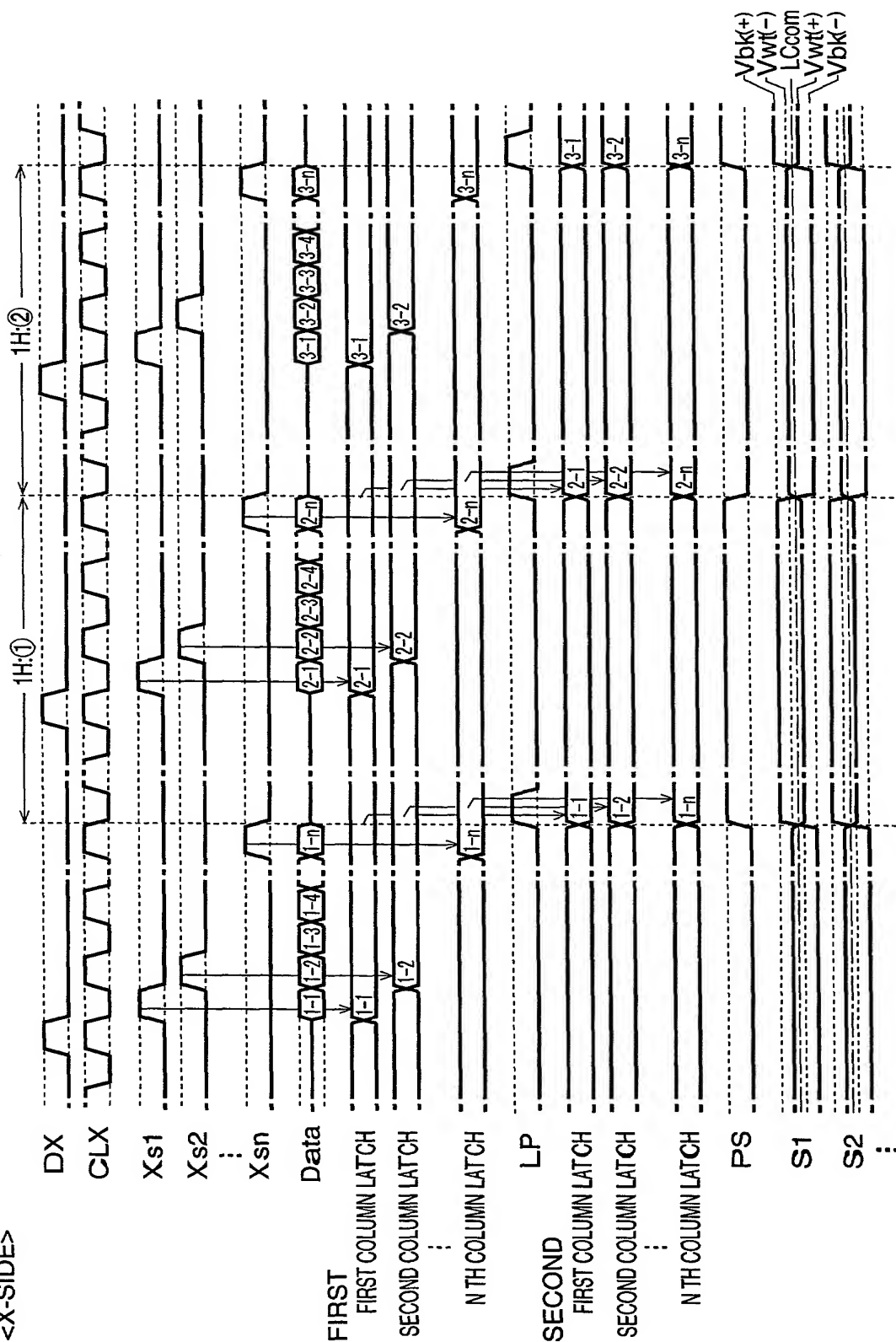
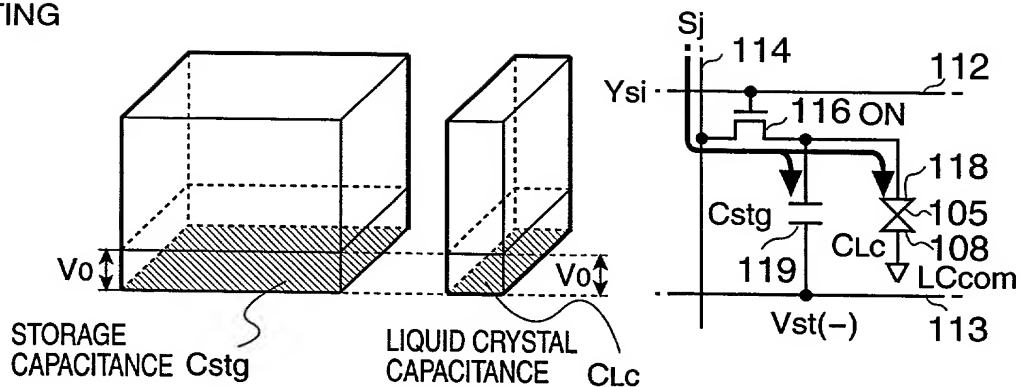
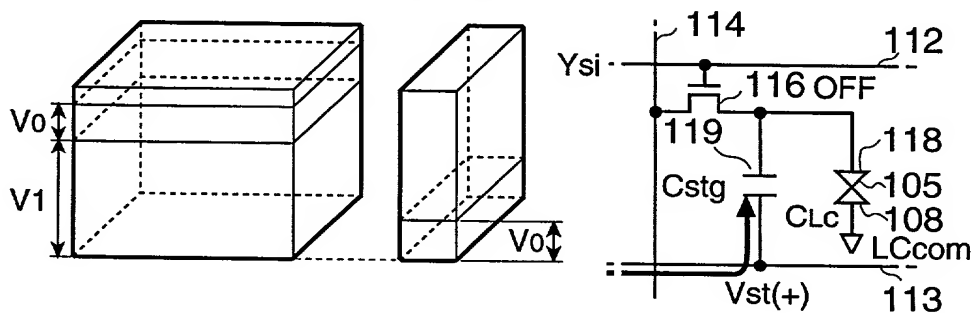


FIG. 4

(a) WRITING



(b) VOLTAGE SHIFT OF STORAGE CAPACITANCE



(c) CHARGE REDISTRIBUTION

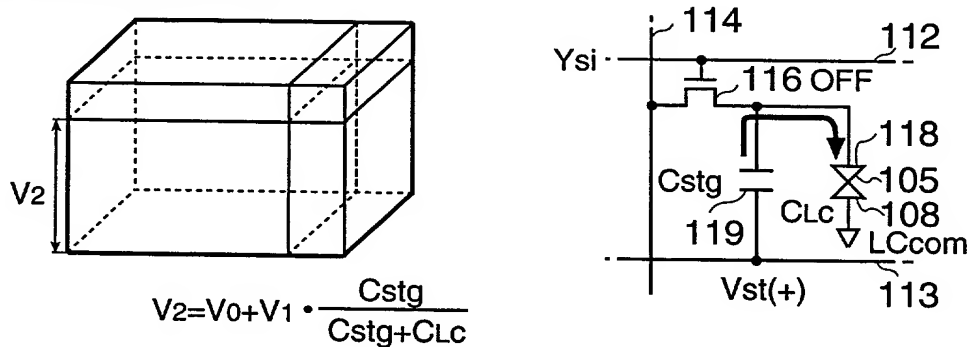
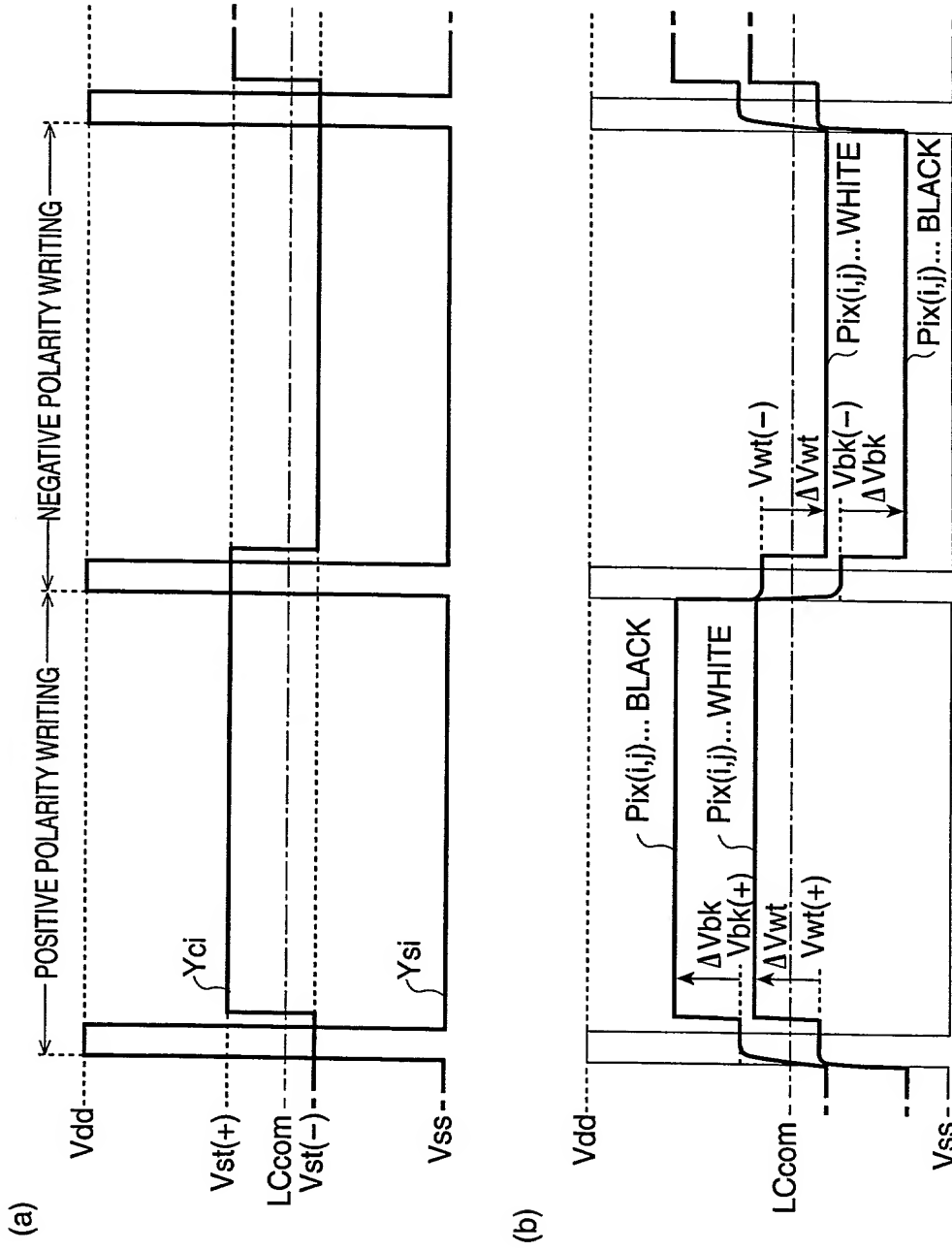


FIG. 5



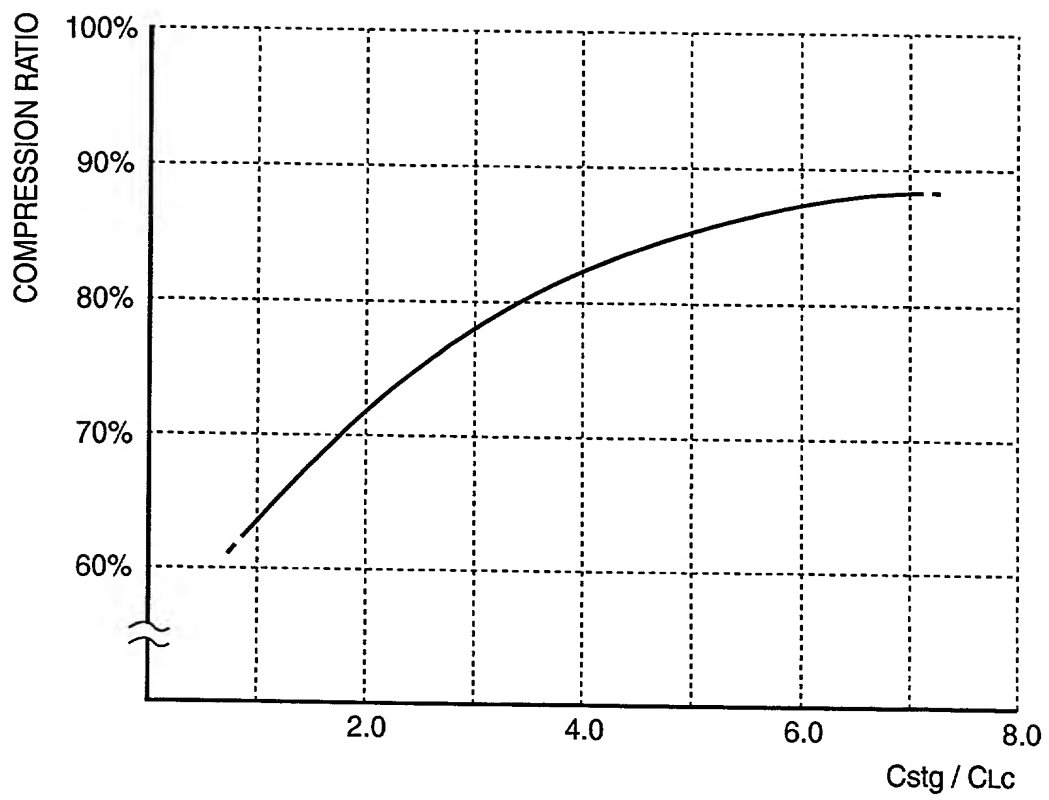


FIG. 7

10040021 22840001

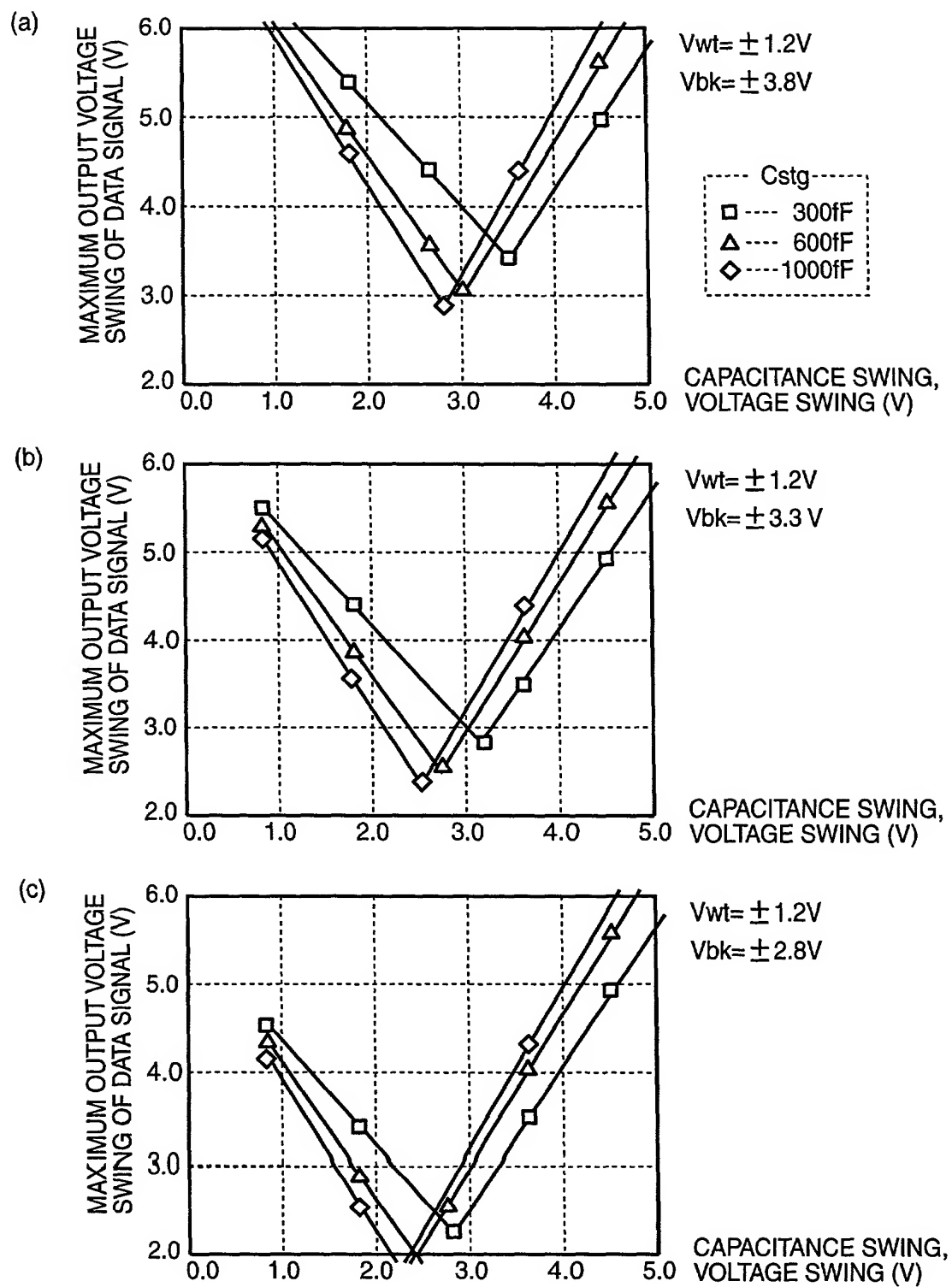


FIG. 8



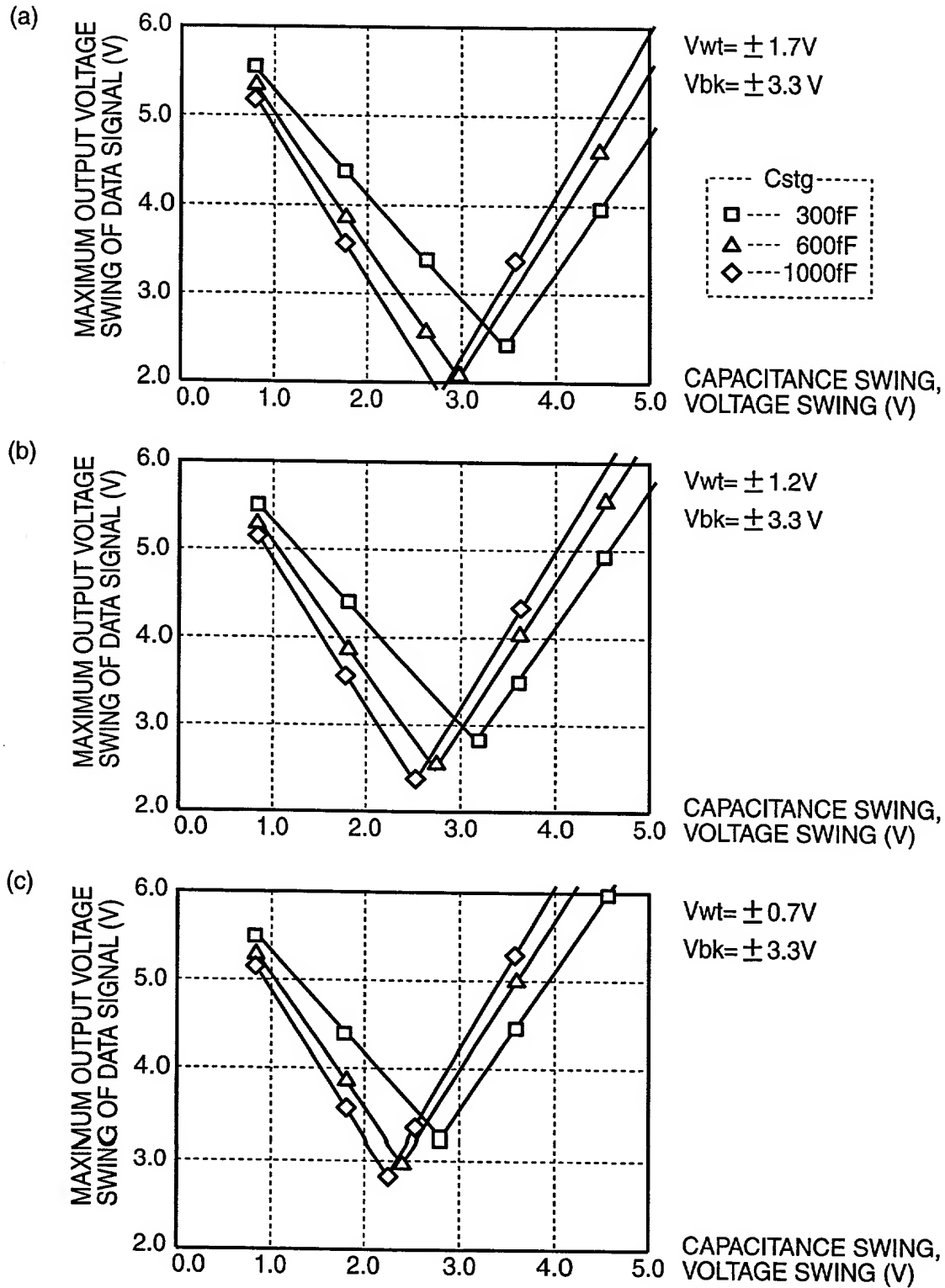


FIG. 9

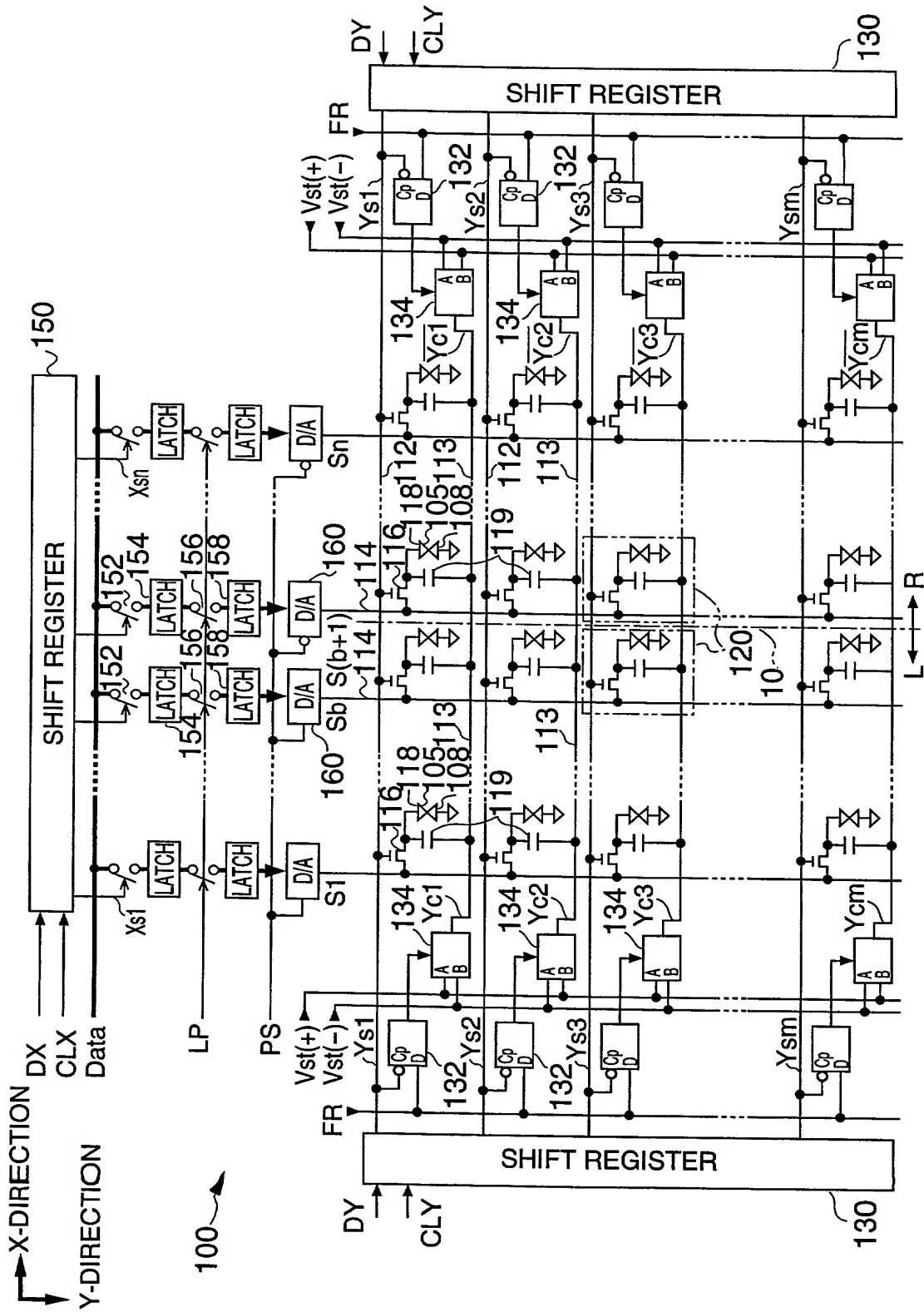


FIG. 10

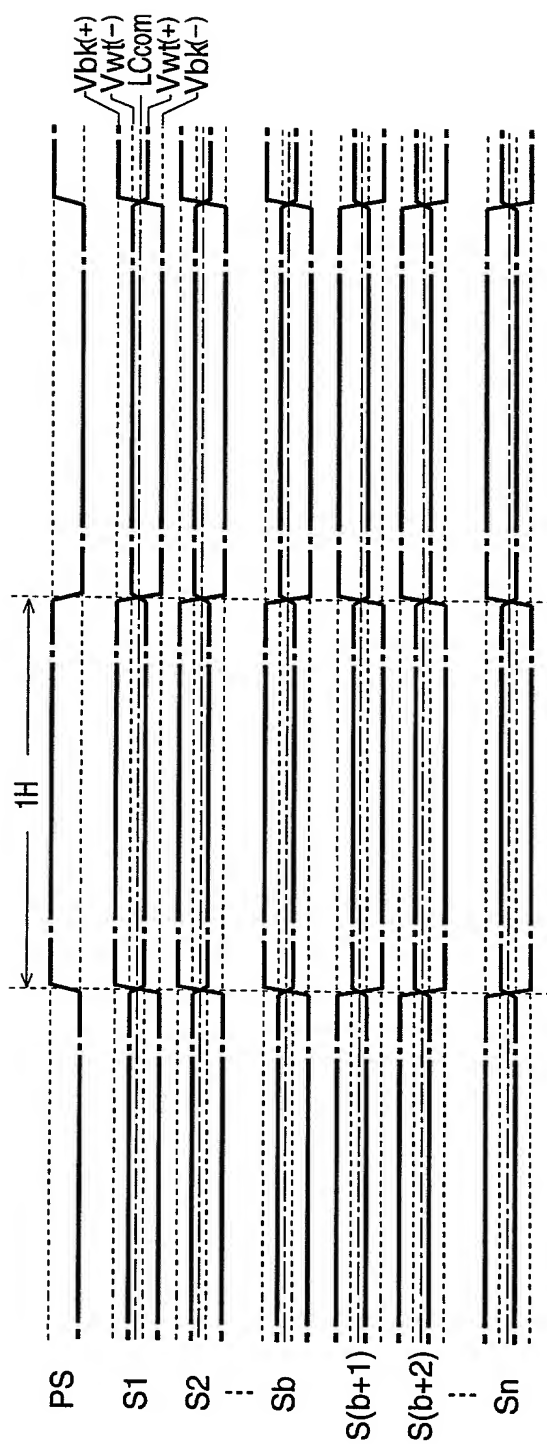


FIG. 11

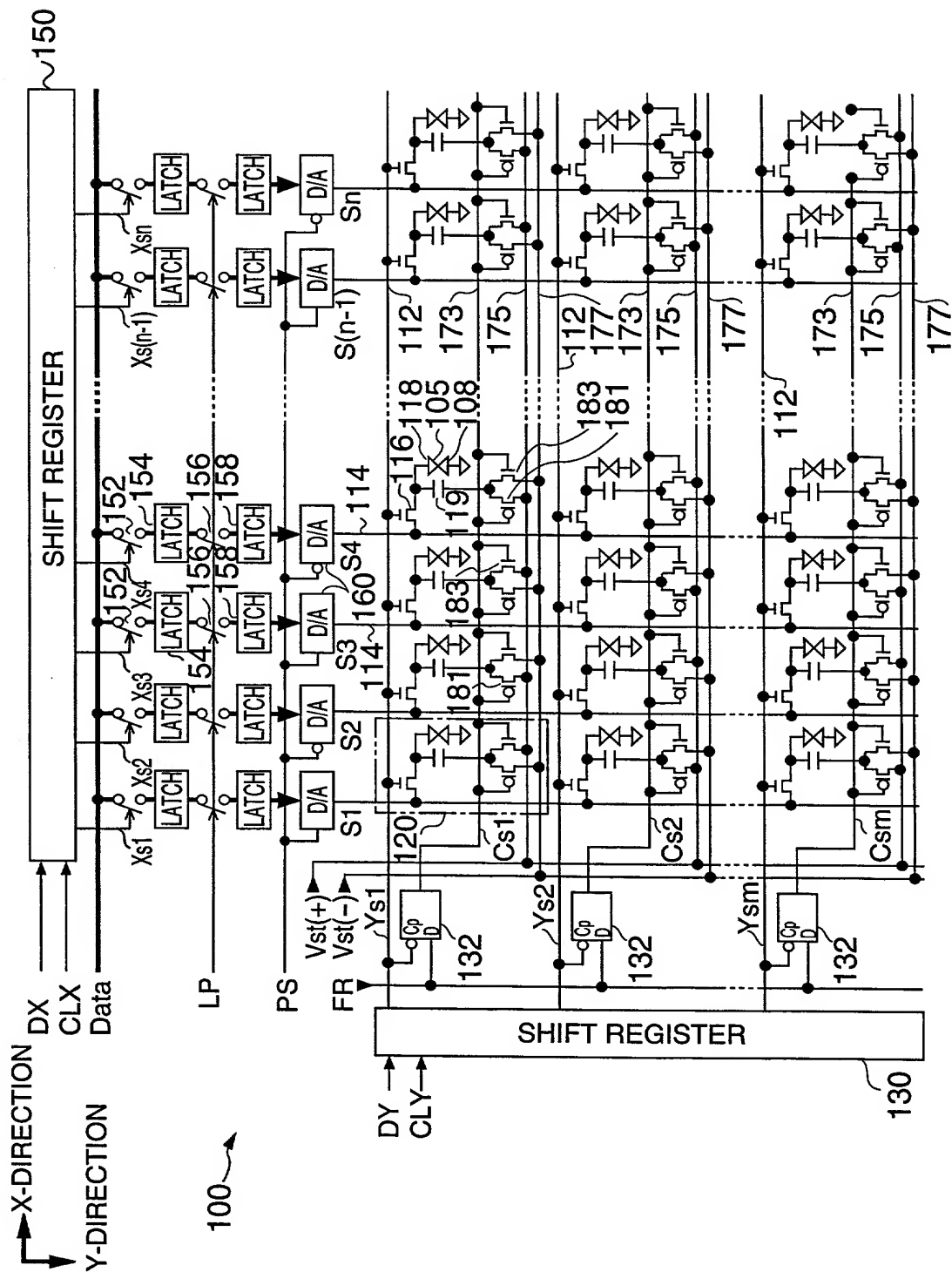


FIG. 12

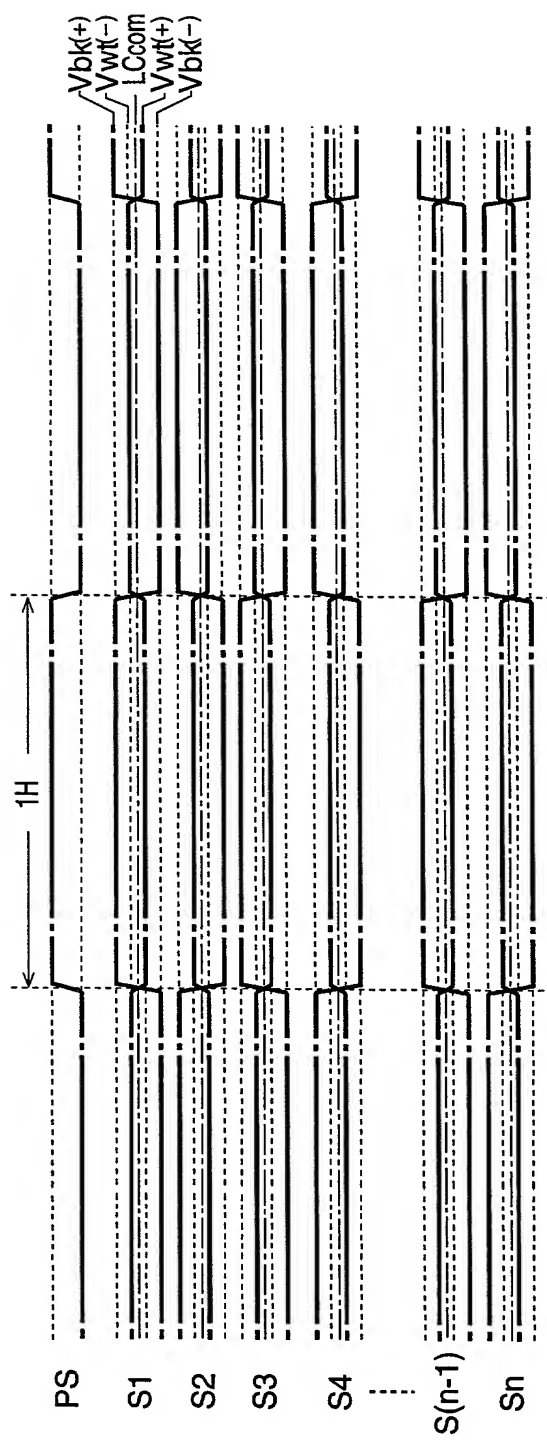


FIG. 13



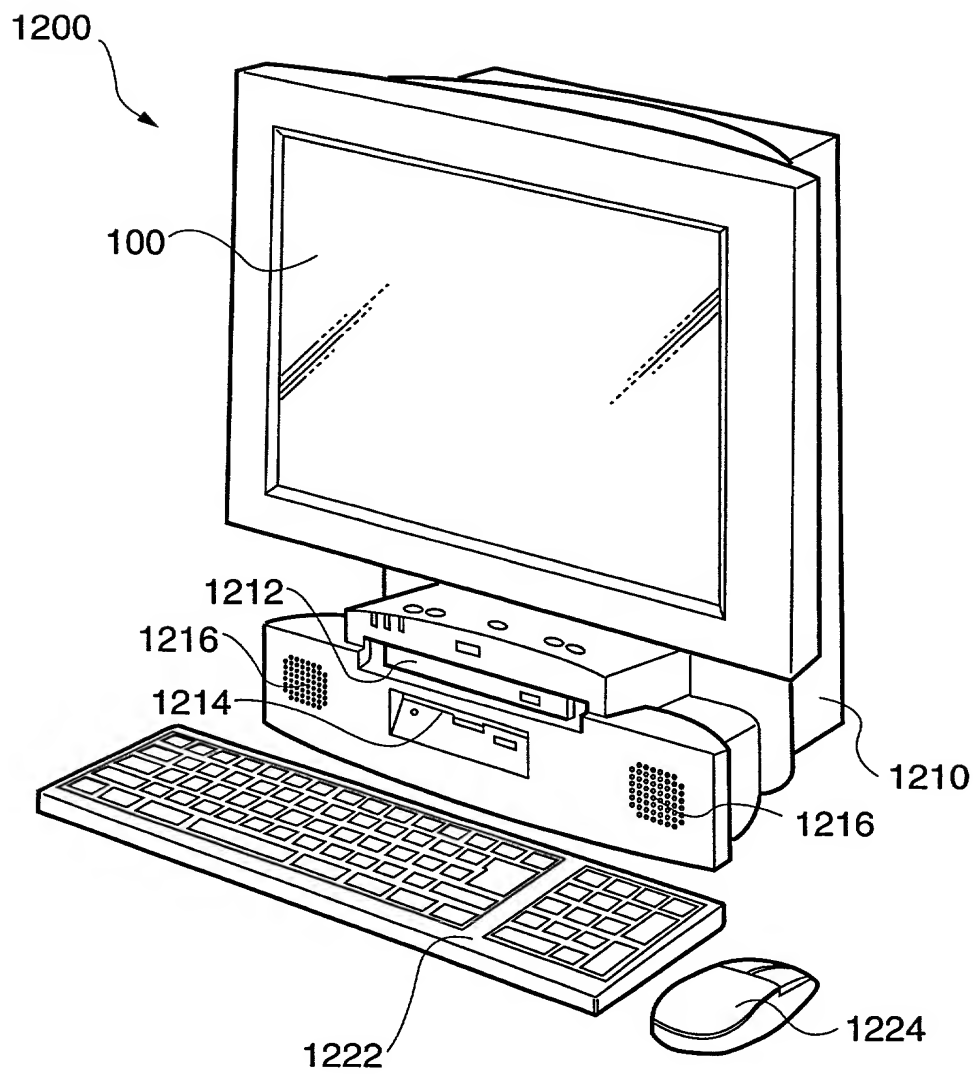


FIG. 15

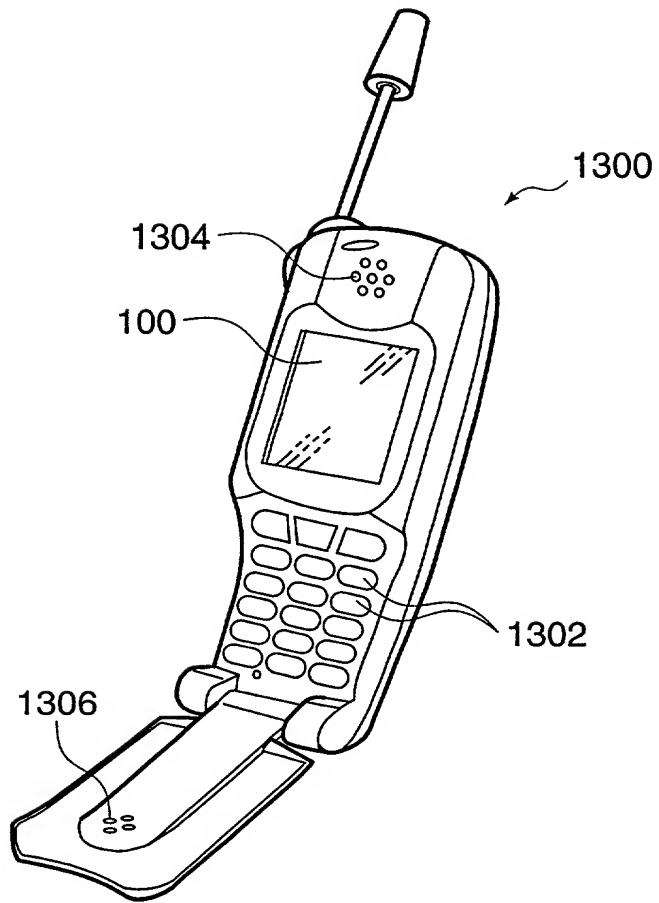


FIG. 16